

Design and characterization of high-speed CMOS pseudo-LVDS transceivers

Tuesday, 6 October 2015 14:30 (30)

The requirements and implementations of transceivers operating at speeds up to several Gb / s and intended for use in on-board data networks are considered. It is noted the possibility of upgrading for a long time used in such networks LVDS - transceivers in order to increase the transmission speed of more than prescribed by the appropriate standard. The results of the characterization of such a pseudo-LVDS transceivers designed in CMOS technology with the norms from 250 nm to 90 nm are presented.

Presentation type

Poster

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Session Classification : Poster Session I

Track Classification : Methods of experimental physics