

Development of the protocol of the interface of data exchange with the GBTX chip

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The structure of the interface of data exchange with the GBTX chip for the CBM experiment is considered. The interface generate data package, consisting of the digital codes of signal amplitude, of signal superposition in peak detector, of signal arrival time and channel number, wherein the event has occurred, generated by the readout blocks of IC. The created data package is coded according to the 8b/10b format for transferring to the GBTX chip. The package of control date (warnings on error and desynchronisation) of registers for a correct exchange (correspondence) under the GBTX protocol. The adjustment of the quantity of channels, generating data packages, connected to the GBTX chip, is possible. The interface has been design according to the 180nm CMOS technology of UMC.

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