

Development of the multichannel data processing ASIC design flow

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An application specific design flow for multichannel analog-digital ICs for front end electronics is presented. The design flow allows to reduce the terms of designing the IC, raise the efficiencies of CAD operation and reach substantially better parameters of the IC in performance, occupied area and power consumption. This flow was successfully applied to the design of the readout ASIC prototype for the CBM muon chambers. The approach was probated in UMC CMOS MMRF process.

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